



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of: **DeWitt, Jr. et al.**

Serial No.: **10/757,269**

Filed: **January 14, 2004**

**For: Method and Apparatus for  
Autonomically Initiating  
Measurement of Secondary Metrics  
Based on Hardware Counter Values  
for Primary Metrics**

**35525**

PATENT TRADEMARK OFFICE  
CUSTOMER NUMBER

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Group Art Unit: **2122**

Examiner: **Unknown**

Attorney Docket No.: **AUS920030549US1**

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P.O. Box 1450  
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Sir:  
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- Supplemental Information Disclosure Statement;
- Form PTO-1449;
- References AG-AI; and
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Respectfully submitted,

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

**DeWitt, Jr. et al.**

Serial No: **10/757,269**

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Title: **Method and Apparatus for  
Autonomically Initiating Measurement of  
Secondary Metrics Based on Hardware  
Counter Values for Primary Metrics**

Group Art Unit: **2122**

Intellectual Property  
Law Department  
International Business  
Machines Corporation  
11400 Burnet Road  
Austin, Texas 78758

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

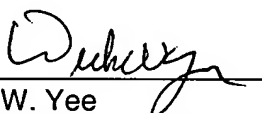
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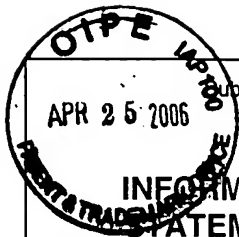
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Respectfully submitted,

  
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

## Complete If Known

Application Number	10/757,269
Filing Date	01/14/2004
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Unknown
Art Unit	2122
Attorney Docket No.	AUS920030549US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5,797,019	08-18-1998	Levine et al.	
AB	6,549,998 B1	04-15-2003	Pekarich et al.	
AC	2003/0101367 A1	05-29-2003	Bartfai et al.	
AD	5,113,507	05-12-1992	Jaekel	
AE	6,681,387 B1	01-20-2004	Hwu et al.	
AF	6,925,424 B2	08-02-2005	Jones et al.	

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

## NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
AG	Wikipedia "JavaServer Pages" downloaded 1/24/06 <a href="http://en.wikipedia.org/wiki/JavaServer_Pages">http://en.wikipedia.org/wiki/JavaServer_Pages</a>
AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
AI	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from <a href="http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html">http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html</a>

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